

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING****COURSE STRUCTURE & SYLLABUS (2016-17)****II YEAR I SEMESTER**

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA301BS	Mathematics – IV	4	1	0	4
2	EC302ES	Analog Electronics	4	1	0	4
3	EC303ES	Electrical Technology	4	1	0	4
4	EC304ES	Signals and Stochastic Process	3	1	0	3
5	EC305ES	Network Analysis	3	1	0	3
6	EC306ES	Electronic Devices and Circuits Lab	0	0	3	2
7	EC307ES	Basic Simulation Lab	0	0	3	2
8	EC308ES	Basic Electrical Engineering Lab	0	0	3	2
9	*MC300ES	Environmental Science and Technology	3	0	0	0
		Total Credits	21	5	9	24

II YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC401ES	Switching Theory and Logic Design	3	1	0	3
2	EC402ES	Pulse and Digital Circuits	4	0	0	4
3	EE404ES	Control Systems	4	1	0	4
4	EC405ES	Analog Communications	4	0	0	4
5	SM405MS	Business Economics and Financial Analysis	3	0	0	3
6	EC406ES	Analog Communications Lab	0	0	3	2
7	EC407ES	Pulse and Digital Circuits Lab	0	0	3	2
8	EC408ES	Analog Electronics Lab	0	0	3	2
9	*MC400HS	Gender Sensitization Lab	0	0	3	0
		Total Credits	18	2	12	24

EC401ES: SWITCHING THEORY AND LOGIC DESIGN

B.Tech. II Year II Sem.

L	T	P	C
3	1	0	3

Course Objectives:

This course provides in-depth knowledge of switching theory and the design techniques of digital circuits, which is the basis for design of any digital circuit. The main objectives are:

- To learn basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
- To understand common forms of number representation in digital electronic circuits and to be able to convert between different representations.
- To implement simple logical operations using combinational logic circuits
- To design combinational logic circuits, sequential logic circuits.
- To impart to student the concepts of sequential circuits, enabling them to analyze sequential systems in terms of state machines.
- To implement synchronous state machines using flip-flops.

Course Outcomes: Upon completion of the course, students should possess the following skills:

- Be able to manipulate numeric information in different forms, e.g. different bases, signed integers, various codes such as ASCII, Gray and BCD.
- Be able to manipulate simple Boolean expressions using the theorems and postulates of Boolean algebra and to minimize combinational functions.
- Be able to design and analyze small combinational circuits and to use standard combinational functions/building blocks to build larger more complex circuits.
- Be able to design and analyze small sequential circuits and devices and to use standard sequential functions/building blocks to build larger more complex circuits.

UNIT – I

Number System and Boolean algebra And Switching Functions: Review of number systems, Complements of Numbers, Codes- Binary Codes, Binary Coded Decimal Code and its Properties, Unit Distance Codes, Error Detecting and Correcting Codes.

Boolean Algebra: Basic Theorems and Properties, Switching Functions, Canonical and Standard Form, Algebraic Simplification of Digital Logic Gates, Properties of XOR Gates, Universal Gates, Multilevel NAND/NOR realizations.

UNIT - II

Minimization and Design of Combinational Circuits: Introduction, The Minimization of switching function using theorem, The Karnaugh Map Method-Up to Five Variable Maps, Don't Care Map Entries, Tabular Method, Design of Combinational Logic: Adders, Subtractors, comparators, Multiplexers, Demultiplexers, Decoders, Encoders and Code converters, Hazards and Hazard Free Relations.

UNIT - III

Sequential Machines Fundamentals and Applications: Introduction: Basic Architectural Distinctions between Combinational and Sequential circuits, The Binary Cell, Fundamentals of Sequential Machine Operation, Latches, Flip Flops: SR, JK, Race Around Condition in JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Design of a Clocked Flip-Flop, Timing and Triggering Consideration, Clock Skew, Conversion from one type of Flip-Flop to another.

Registers and Counters: Shift Registers, Data Transmission in Shift Registers, Operation of Shift Registers, Shift Register Configuration, Bidirectional Shift Registers, Applications of Shift Registers, Design and Operation of Ring and Twisted Ring Counter, Operation Of Asynchronous And Synchronous Counters.

UNIT - IV

Sequential Circuits - I: Introduction, State Diagram, Analysis of Synchronous Sequential Circuits, Approaches to the Design of Synchronous Sequential Finite State Machines, Synthesis of Synchronous Sequential Circuits, Serial Binary Adder, Sequence Detector, Parity-bit Generator, Design of Asynchronous Counters, Design of Synchronous Modulo N – Counters.

UNIT - V

Sequential Circuits - II: Finite state machine-capabilities and limitations, Mealy and Moore models-minimization of completely specified and incompletely specified sequential machines, Partition techniques, and Merger chart methods-concept of minimal cover table.

TEXT BOOKS:

1. Switching and Finite Automata Theory- Zvi Kohavi & Niraj K. Jha, 3rd Edition, Cambridge.
2. Digital Design- Morris Mano, 5rd Edition, Pearson.

REFERENCE BOOKS:

1. Modern Digital electronics RP Jain 4th Edition, McGraw Hill
2. Switching Theory and Logic Design – A Anand Kumar, 3rd Edition, PHI, 2013.

EC402ES: PULSE AND DIGITAL CIRCUITS

B.Tech. II Year II Sem.

L	T	P	C
4	0	0	4

Course Objectives:

- To explain the complete response of R-C and R-L-C transient circuits.
- To explain clippers, clampers, switching characteristics of transistors and sampling gates.
- To construct various multivibrators using transistors, design of sweep circuits and sampling gates.
- To discuss and realize logic gates using diodes and transistors.

Course Outcomes: At the end of the course, the student will be able to:

- Understand the applications of diode as integrator, differentiator, clippers, clamper circuits.
- Learn various switching devices such as diode, transistor, SCR. Difference between logic gates and sampling gates
- Design multivibrators for various applications, synchronization techniques and sweep circuits.
- Realizing logic gates using diodes and transistors.
- Understanding of time and frequency domain aspects.
- Importance of clock pulse and its generating techniques.

UNIT - I

Linear Wave Shaping: High pass and low pass RC circuits and their response for Sinusoidal, Step, Pulse, Square, & Ramp inputs, High pass RC network as Differentiator, Low pass RC circuit as an Integrator, Attenuators and its application as a CRO Probe, RL and RLC Circuits and their response for Step Input, Ringing Circuit.

UNIT - II

Non-Linear Wave Shaping: Diode clippers, Transistor clippers, Clipping at two independent levels, Comparators, Applications of Voltage comparators. Clamping Operation, Clamping circuit taking Source and Diode resistances into account, Clamping Circuit Theorem, Practical Clamping Circuits, Effect of Diode Characteristics on Clamping Voltage, Synchronized Clamping.

UNIT - III

Switching Characteristics of Devices: Diode as a Switch, Piecewise Linear Diode Characteristics, Diode Switching times, Transistor as a Switch, Break down voltages, Transistor in Saturation, Temperature variation of Saturation Parameters, Transistor-switching times, Silicon-controlled-switch circuits.

UNIT – IV

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors.

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, Transistor Miller Time Base generator, Transistor Bootstrap Time Base Generator, Transistor Current Time Base Generators, Methods of Linearity improvement.

UNIT - V

Sampling Gates: Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL and CML Logic Families and its Comparison.

TEXT BOOKS:

1. Millman's Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2 Ed., 2008, McGraw Hill.
2. Pulse, Switching and Digital Circuits – David A. Bell, 5th edition 2015, OXFORD University Press

REFERENCE BOOKS:

1. Pulse and Digital Circuits -Venkata Rao K, Rama Sudha K, Manmadha rao G, Pearson, 2010
2. Pulse and Digital Circuits – A. Anand Kumar, 2005, PHI.

SM405ES: BUSINESS ECONOMICS AND FINANCIAL ANALYSIS

B.Tech. II Year II Sem.

L T P C
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Course Objective: To learn the basic Business types, impact of the Economy on Business and Firms specifically. To analyze the Business from the Financial Perspective.

Course Outcome: The students will understand the various Forms of Business and the impact of economic variables on the Business. The Demand, Supply, Production, Cost, Market Structure, Pricing aspects are learnt. The Students can study the firm's financial position by analysing the Financial Statements of a Company.

UNIT – I

Introduction to Business and Economics:

Business: Structure of Business Firm, Theory of Firm, Types of Business Entities, Limited Liability Companies, Sources of Capital for a Company, Non-Conventional Sources of Finance.

Economics: Significance of Economics, Micro and Macro Economic Concepts, Concepts and Importance of National Income, Inflation, Money Supply in Inflation, Business Cycle, Features and Phases of Business Cycle. Nature and Scope of Business Economics, Role of Business Economist, Multidisciplinary nature of Business Economics.

UNIT – II

Demand and Supply Analysis:

Elasticity of Demand: Elasticity, Types of Elasticity, Law of Demand, Measurement and Significance of Elasticity of Demand, Factors affecting Elasticity of Demand, Elasticity of Demand in decision making, Demand Forecasting: Characteristics of Good Demand Forecasting, Steps in Demand Forecasting, Methods of Demand Forecasting.

Supply Analysis: Determinants of Supply, Supply Function & Law of Supply.

UNIT- III

Production, Cost, Market Structures & Pricing:

Production Analysis: Factors of Production, Production Function, Production Function with one variable input, two variable inputs, Returns to Scale, Different Types of Production Functions.

Cost analysis: Types of Costs, Short run and Long run Cost Functions.

Market Structures: Nature of Competition, Features of Perfect competition, Monopoly, Oligopoly, and Monopolistic Competition.

Pricing: Types of Pricing, Product Life Cycle based Pricing, Break Even Analysis, and Cost Volume Profit Analysis.

UNIT - IV

Financial Accounting: Accounting concepts and Conventions, Accounting Equation, Double-Entry system of Accounting, Rules for maintaining Books of Accounts, Journal, Posting to Ledger, Preparation of Trial Balance, Elements of Financial Statements, and Preparation of Final Accounts.

UNIT - V

Financial Analysis through Ratios: Concept of Ratio Analysis, Liquidity Ratios, Turnover Ratios, Profitability Ratios, Proprietary Ratios, Solvency, Leverage Ratios (simple problems). Introduction to Fund Flow and Cash Flow Analysis (simple problems).

TEXT BOOKS:

1. D. D. Chaturvedi, S. L. Gupta, Business Economics - Theory and Applications, International Book House Pvt. Ltd. 2013.
2. Dhanesh K Khatri, Financial Accounting, Tata McGraw Hill, 2011.
3. Geethika Ghosh, Piyali Gosh, Purba Roy Choudhury, Managerial Economics, 2e, Tata McGraw Hill Education Pvt. Ltd. 2012.

REFERENCES:

1. Paresh Shah, Financial Accounting for Management 2e, Oxford Press, 2015.
2. S. N. Maheshwari, Sunil K Maheshwari, Sharad K Maheshwari, Financial Accounting, 5e, Vikas Publications, 2013.

EE404ES: CONTROL SYSTEMS

B.Tech. II Year II Sem.

L	T	P	C
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Prerequisite: Ordinary Differential Equations & Laplace Transform, Mathematics I

Course objectives:

- To understand the different ways of system representations such as Transfer function representation and state space representations and to assess the system dynamic response
- To assess the system performance using time domain analysis and methods for improving it
- To assess the system performance using frequency domain analysis and techniques for improving the performance
- To design various controllers and compensators to improve system performance

Course outcomes: After completion of this course the student is able to

- Improve the system performance by selecting a suitable controller and/or a compensator for a specific application
- Apply various time domain and frequency domain techniques to assess the system performance
- Apply various control strategies to different applications (example: Power systems, electrical drives etc...)
- Test system Controllability and Observability using state space representation and applications of state space representation to various systems.

UNIT – I

Introduction: Concepts of Control Systems- Open Loop and closed loop control systems and their differences- Different examples of control systems- Classification of control systems, Feed-Back Characteristics, Effects of feedback. Mathematical models – Differential equations - Impulse Response and transfer functions - Translational and Rotational mechanical systems.

Transfer Function Representation: Transfer Function of DC Servo motor - AC Servo motor- Synchro transmitter and Receiver, Block diagram representation of systems considering electrical systems as examples - Block diagram algebra – Representation by Signal flow graph - Reduction using mason's gain formula.

UNIT-II

Time Response Analysis: Standard test signals - Time response of first order systems – Characteristic Equation of Feedback control systems, Transient response of second order systems - Time domain specifications – Steady state response - Steady state errors and error constants – Effects of proportional derivative, proportional integral systems.

UNIT – III

Stability Analysis: The concept of stability - Routh stability criterion – qualitative stability and conditional stability.

Root Locus Technique: The root locus concept - construction of root loci-effects of adding poles and zeros to $G(s)H(s)$ on the root loci.

Frequency Response Analysis: Introduction, Frequency domain specifications-Bode diagrams-Determination of Frequency domain specifications and transfer function from the Bode Diagram-Phase margin and Gain margin-Stability Analysis from Bode Plots.

UNIT - IV

Stability Analysis In Frequency Domain: Polar Plots, Nyquist Plots and applications of Nyquist criterion to find the stability - Effects of adding poles and zeros to $G(s)H(s)$ on the shape of the Nyquist diagrams.

Classical Control Design Techniques: Compensation techniques – Lag, Lead, and Lead-Lag Controllers design in frequency Domain, PID Controllers.

UNIT – V

State Space Analysis of Continuous Systems: Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization- Solving the Time invariant state Equations- State Transition Matrix and its Properties.

TEXT BOOKS:

1. “I. J. Nagrath and M. Gopal”, “Control Systems Engineering”, New Age International (P) Limited, Publishers, 5th edition, 2009
2. “B. C. Kuo”, “Automatic Control Systems”, John wiley and sons, 8th edition, 2003.

REFERENCE BOOKS:

1. “N. K. Sinha”, “Control Systems”, New Age International (P) Limited Publishers, 3rd Edition, 1998.
2. “NISE”, “Control Systems Engineering”, John wiley, 6th Edition, 2011.
3. “Katsuhiko Ogata”, “Modern Control Engineering”, Prentice Hall of India Pvt. Ltd., 3rd edition, 1998.

EC405ES: ANALOG COMMUNICATIONS

B.Tech. II Year II Sem.

L	T	P	C
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Course Objectives:

- To develop ability to analyze system requirements of analog communication systems.
- To understand the need for modulation
- To understand the generation, detection of various analog modulation techniques and also perform the mathematical analysis associated with these techniques.
- To acquire knowledge to analyze the noise performance of analog modulation techniques.
- To acquire theoretical knowledge of each block in AM and FM receivers.
- To understand the pulse modulation techniques.

Course Outcomes:

- Able to analyze and design various modulation and demodulation analog systems.
- Understand the characteristics of noise present in analog systems.
- Study of signal to Noise Ratio (SNR) performance, of various Analog Communication systems.
- Analyze and design the various Pulse Modulation Systems.
- Understand the concepts of Multiplexing: Time Division Multiplexing (TDM) and Frequency Division Multiplexing (FDM).

UNIT - I

Amplitude Modulation: Introduction to communication system, Need for modulation, Frequency Division Multiplexing, Amplitude Modulation, Definition, Time domain and frequency domain description, single tone modulation, power relations in AM waves, Generation of AM waves, square law Modulator, Switching modulator, Detection of AM Waves; Square law detector, Envelope detector, Double side band suppressed carrier modulators, time domain and frequency domain description, Generation of DSBSC Waves, Balanced Modulators, Ring Modulator, Coherent detection of DSB-SC Modulated waves, COSTAS Loop.

UNIT - II

SSB Modulation: Introduction to Hilbert Transform, Frequency domain description, Frequency discrimination method for generation of AM SSB Modulated Wave, Time domain description, Phase discrimination method for generating AM SSB Modulated waves. Demodulation of SSB Waves, Vestigial side band modulation: Frequency description, Generation of VSB Modulated wave, Time domain description, Envelope detection of a VSB Wave pulse Carrier, Comparison of AM Techniques, Applications of different AM Systems.

UNIT - III

Angle Modulation: Basic concepts, Frequency Modulation: Single tone frequency modulation, Spectrum Analysis of Sinusoidal FM Wave, Narrow band FM, Wide band FM, Constant Average Power, Transmission bandwidth of FM Wave - Generation of FM Waves, Direct FM, Detection of FM Waves: Balanced Frequency discriminator, Zero crossing detector, Phase locked loop, Comparison of FM and AM.

UNIT - IV

Noise: Resistive Noise Source (Thermal), Arbitrary Noise Sources, Effective Noise Temperature, Average Noise Figures, Average Noise Figure of cascaded networks, Narrow Band noise, Quadrature representation of narrow band noise, & its properties

Noise in Analog communication System, Noise in DSB and SSB System Noise in AM System, Noise in Angle Modulation System, Threshold effect in Angle Modulation System, Pre-emphasis and de-emphasis.

UNIT - V

Receivers: Radio Receiver - Receiver Types - Tuned radio frequency receiver, Super heterodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, AGC, FM Receiver, Comparison with AM Receiver, Amplitude limiting.

PULSE MODULATION: Types of Pulse modulation, PAM (Single polarity, double polarity) PWM: Generation and demodulation of PWM, PPM, Generation and demodulation of PPM, Time Division Multiplexing.

TEXTBOOKS:

1. Communication Systems by Simon Haykins John Wiley & Sons, 4th Edition.
2. Electronics & Communication System – George Kennedy and Bernard Davis, McGraw Hill Education 2004.

REFERENCES:

1. Communication theory, thomas, 2 edition, McGraw-Hill Education
2. Communication Systems, 2E, R. P. Singh, S. D. Sapre, McGraw-Hill Education, 2008.
3. Analog and Digital Communication – K. Sam Shanmugam, Willey, 2005
4. Electronics Communication Systems- Wayne Tomasi, 6th Edition, Person 2009

EC406ES: ANALOG COMMUNICATIONS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	2

Note:

- Minimum 12 experiments should be conducted:
- Experiments are to be simulated first either using MATLAB, Comsim or any other simulation software tools and then testing to be done in hardware.

LIST OF EXPERIMENTS:

1. Amplitude modulation and demodulation.
2. DSB-SC Modulator & Detector
3. SSB-SC Modulator & Detector (Phase Shift Method)
4. Frequency modulation and demodulation.
5. Study of spectrum analyzer and analysis of AM and FM Signals
6. Pre-emphasis & de-emphasis.
7. Time Division Multiplexing & De multiplexing
8. Frequency Division Multiplexing & De multiplexing
9. Verification of Sampling Theorem
10. Pulse Amplitude Modulation & Demodulation
11. Pulse Width Modulation & Demodulation
12. Pulse Position Modulation & Demodulation
13. Frequency Synthesizer.
14. AGC Characteristics.
15. PLL as FM Demodulator

EC407ES: PULSE AND DIGITAL CIRCUITS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	2

Note:

Minimum Twelve experiments to be conducted:

1. Linear wave Shaping
 - a. RC Low Pass Circuit for different time constants
 - b. RC High Pass Circuit for different time constants
2. Non-linear wave shaping
 - a. Transfer characteristics and response of Clippers:
 - i) Positive and Negative Clippers
 - ii) Clipping at two independent levels
 - b. The steady state output waveform of clampers for a square wave input
 - i) Positive and Negative Clampers
 - ii) Clamping at different reference voltage
3. Comparison Operation of different types of Comparators
4. Switching characteristics of a transistor
5. Design a Bistable Multivibrator and draw its waveforms
6. Design an Astable Multivibrator and draw its waveforms
7. Design a Monostable Multivibrator and draw its waveforms
8. Response of Schmitt Trigger circuit for loop gain less than and greater than one
9. UJT relaxation oscillator
10. The output- voltage waveform of Boot strap sweep circuit
11. The output- voltage waveform of Miller sweep circuit
12. Pulse Synchronization of An Astable circuit
13. Response of a transistor Current sweep circuit
14. Sampling gates
 - a. Response of Unidirectional gate
 - b. Response of Bidirectional gate using transistors
15. Study of logic gates

EC408ES: ANALOG ELECTRONICS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	2

Note:

- Minimum 12 experiments should be conducted:
- Experiments are to be simulated using Multisim or P-spice or Equivalent Simulation and then testing to be done in hardware.

LIST OF EXPERIMENTS:

1. Common Emitter Amplifier
2. Common Base Amplifier
3. Common Source amplifier
4. Two Stage RC Coupled Amplifier
5. Current Shunt Feedback Amplifier
6. Voltage Series Feedback Amplifier
7. Cascode Amplifier
8. Wien Bridge Oscillator using Transistors
9. RC Phase Shift Oscillator using Transistors
10. Class A Power Amplifier (Transformer less)
11. Class B Complementary Symmetry Amplifier
12. Hartley Oscillator
13. Colpitt's Oscillator
14. Single Tuned Voltage Amplifier

MC400HS: GENDER SENSITIZATION LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	0

Course Objectives:

- To develop students' sensibility with regard to issues of gender in contemporary India.
- To provide a critical perspective on the socialization of men and women.
- To introduce students to information about some key biological aspects of genders.
- To expose the students to debates on the politics and economics of work.
- To help students reflect critically on gender violence.
- To expose students to more egalitarian interactions between men and women.

Course Outcomes:

- Students will have developed a better understanding of important issues related to gender in contemporary India.
- Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature, and film.
- Students will attain a finer grasp of how gender discrimination works in our society and how to counter it.
- Students will acquire insight into the gendered division of labour and its relation to politics and economics.
- Men and women students and professionals will be better equipped to work and live together as equals.
- Students will develop a sense of appreciation of women in all walks of life.
- Through providing accounts of studies and movements as well as the new laws that provide protection and relief to women, the textbook will empower students to understand and respond to gender violence.

UNIT - I

UNDERSTANDING GENDER

Gender: Why Should We Study It? (*Towards a World of Equals*: Unit -1)

Socialization: Making Women, Making Men (*Towards a World of Equals*: Unit -2)

Introduction. Preparing for Womanhood. Growing up Male. First lessons in Caste. Different Masculinities.

UNIT - II

GENDER AND BIOLOGY:

Missing Women: Sex Selection and Its Consequences (*Towards a World of Equals*: Unit -4)
Declining Sex Ratio. Demographic Consequences.

Gender Spectrum: Beyond the Binary (*Towards a World of Equals*: Unit -10)

Two or Many? Struggles with Discrimination.

UNIT - III

GENDER AND LABOUR

Housework: the Invisible Labour (*Towards a World of Equals*: Unit -3)

“My Mother doesn’t Work.” “Share the Load.”

Women’s Work: Its Politics and Economics (*Towards a World of Equals*: Unit -7)

Fact and Fiction. Unrecognized and Unaccounted work. Additional Reading: Wages and Conditions of Work.

UNIT-IV

ISSUES OF VIOLENCE

Sexual Harassment: Say No! (*Towards a World of Equals*: Unit -6)

Sexual Harassment, not Eve-teasing- Coping with Everyday Harassment- Further Reading: “Chupulu”.

Domestic Violence: Speaking Out (*Towards a World of Equals*: Unit -8)

Is Home a Safe Place? -When Women Unite [Film]. Rebuilding Lives. Additional Reading: New Forums for Justice.

Thinking about Sexual Violence (*Towards a World of Equals*: Unit -11)

Blaming the Victim-“I Fought for my Life....” - Additional Reading: The Caste Face of Violence.

UNIT - V

GENDER: CO - EXISTENCE

Just Relationships: Being Together as Equals (*Towards a World of Equals*: Unit -12)

Mary Kom and Onler. Love and Acid just do not Mix. Love Letters. Mothers and Fathers. Additional Reading: Rosa Parks-The Brave Heart.

TEXTBOOK

All the five Units in the Textbook, “*Towards a World of Equals: A Bilingual Textbook on Gender*” written by A. Suneetha, Uma Bhugubanda, Duggirala Vasanta, Rama Melkote, Vasudha Nagaraj, Asma Rasheed, Gogu Shyamala, Deepa Sreenivas and Susie Tharu and published by **Telugu Akademi, Hyderabad, Telangana State** in the year **2015**.

Note: Since it is an Interdisciplinary Course, Resource Persons can be drawn from the fields of English Literature or Sociology or Political Science or any other qualified faculty who has expertise in this field from engineering departments.

REFERENCE BOOKS:

1. Menon, Nivedita. Seeing like a Feminist. New Delhi: Zubaan-Penguin Books, 2012
2. Abdulali Sohaila. “*I Fought For My Life...and Won.*” Available online at: <http://www.thealternative.in/lifestyle/i-fought-for-my-lifeand-won-sohaila-abdulali/>